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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Chen et al.

Serial No.: 10/697,589

Filed: October 30, 2003

For: METHOD FOR MAKING
NANOSCALE WIRES AND
GAPS FOR SWITCHES AND
TRANSISTORS

Examiner: WEISS, H.

Art Unit: 2814

Confirmation No.: 8317

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENTS AND REMARKS

Dear Sir:

In response to the Office Action mailed January 31, 2006, please consider the following remarks and enter the following amendments.

Amendments to the claims are presented beginning on page 2 of this response. Remarks begin on page 5 of this response.